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** CONTINUING DATA *****

 This application is a DIV of 09/980,891 12/03/2001
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** FOREIGN APPLICATIONS *****

JAPAN 2000-101867 04/04/2000

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Verified and Acknowledged Examiner's Signature: <i>[Signature]</i> Initials: <i>[Initials]</i>				

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TITLE

Method and apparatus for defect analysis of semiconductor integrated circuit

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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